BHAVAN'S VIVEKANANDA COLLEGE, SAINIKPURI

Final Practical Time Table for Physical Sciences Sem 6 (2019 - 20) 10:00 am to 12:00 Noon

	24-09-2020	25-09-2020	26-09-2020	28-09-2020	29-09-2020	30-09-2020	01-10-2020	03-10-2020
MPCS3		Computer Networks	Web Technologi es	Modern Physics ONE	Electronics ONE			Numerical Analysis &
		ONE, TWO THREE	ONE, TWO, THREE	Electronics THREE	Modern Physics THREE			Solid Geometry
				Electronics FIVE	Modern Physics FIVE			E Caralana
								Entire class
MECS3	DSD Using VHDL ONE	8051 Microcontroller ONE		Computer Networks	Web Technologi es			Numerical Analysis &
		DSD Using VHDL	8051 Microcontroller THREE	ONE, TWO THREE	ONE, TWO THREE			Solid Geometry
		8051 Microcontroller	DSD Using VHDL					
							-	Entire class
MSCS3				Applied Statistics II	ш	Computer Networks	Web Technolog es	Analysis &
				& Operations Research		A SECTION	A SECTION	Solid Geometry
				h				
				Entire class				Entire class

NOTE:

- 1 Students having Backlog examinations in practicals are requested to report to the concerned HOD
- 2 ONE/ TWO/ THREE/ FOUR BATCHES OF A SECTION

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FIVE/ SIX - B SECTION BATCHES

23/9

Final Practical Time Table for Physical Sciences Sem 6 (2019 - 20) 2:00 pm to 4:00 pm

	24-09-2020	25-09-2020	26-09-2020	28-09-2020	29-09-2020	30-09-2020	01-10-2020	03-10-2020
MPCS3			ll se	Modern Physics TWO	Electronics TWO			
		Computer Networks	Web Technologi es	Electronics FOUR	Modern Physics FOUR		· · · · · · · · · · · · · · · · · · ·	
		FOUR, FIVE, SIX	FOUR, FIVE, SIX	Modern Physics	Electronics SIX			
MECS3	DSD Using VHDL	8051 Microcontroller	ī					
		DSD Using VHDL FOUR	8051 Microcontroller FOUR	Computer Networks	Web Technologi es			
		8051 Microcontroller	DSD Using VHDL SIX	FOUR, FIVE, SIX	FOUR, FIVE, SIX			
MSCS3			10			i		
	-					Computer Networks	Web Technologi es	
						B SECTION	B SECTION	

NOTE:

1 Students having Backlog examinations in practicals are requested to report to the concerned HOD

2 ONE/ TWO/ THREE/ FOUR - BATCHES OF A SECTION

FIVE/ SIX - B SECTION BATCHES

aSc Timetables

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